

WHAT CLAIMED IS:

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1. A signal processing apparatus comprising:
a digital signal processor comprising an
internal memory part storing a program to be
executed;

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an external memory part storing programs
executable in said digital signal processor;

a clock signal generating part generating a
clock signal and outputting the clock signal to said
digital signal processor; and

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a clock signal control part controlling
outputting of said clock signal to said digital
signal processor so that said programs stored in said
external memory part can be forwarded to said
internal memory part.

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2. The signal processing apparatus as
25 claimed in claim 1, wherein said clock signal control

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part forwards said programs read from said external memory part to said internal memory after stopping outputting said clock signal to said digital signal processor.

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3. The signal processing apparatus as
10 claimed in claim 1, wherein said clock signal control part comprises a forward circuit part and a clock control part, said clock control part stops outputting said clock signal to said digital signal processor after said forward circuit part supplies
15 said clock control part with a signal requesting that said clock control part stops outputting said clock signal to said digital signal processor so that said programs stored in said external memory part can be forwarded to said internal memory part.

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4. The signal processing apparatus as
25 claimed in claim 3, wherein said clock control part

restarts outputting said clock signal to said digital
signal processor after said forward circuit part
supplies said clock control part with a signal
requesting that said clock control part outputs said
5 clock signal to said digital signal processor when
said programs stored in said external memory part are
completely forwarded to said internal memory part.

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5. The signal processing apparatus as
claimed in claim 1, wherein said clock signal control
part controls outputting of said clock signal to said
15 digital signal processor in compliance with a request
from said digital signal processor.

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6. The signal processing apparatus as
claimed in claim 1, wherein said clock signal control
part controls outputting of said clock signal to said
digital signal processor in compliance with a request
25 from an outside of said signal processing apparatus.

7. The signal processing apparatus as claimed in claim 1, wherein said clock signal control part comprises a forward circuit for forwarding a desired part of said programs read from said external
5 memory part to said internal memory.

10 8. A modem for modulating/demodulating a communication data by using a signal processing apparatus comprising:

a digital signal processor comprising an internal memory part storing a program to be
15 executed;

an external memory part storing programs executable in said digital signal processor;

a clock signal generating part for generating a clock signal and outputting the clock
20 signal to said digital signal processor to said digital signal processor; and

a clock signal control part controlling outputting of said clock signal to said digital signal processor so that said programs stored in said
25 external memory part can be forwarded to said

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internal memory part.

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9. The modem as claimed in 8, wherein said clock signal control part forwards said programs read from said external memory part to said internal memory after stopping outputting said clock signal to said digital signal processor.

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10. The modem as claimed in 8, wherein said clock signal control part comprises a forward circuit part and a clock control part, said clock control part stops outputting said clock signal to said digital signal processor after said forward circuit part supplies said clock control part with a signal requesting that said clock control part stops outputting said clock signal to said digital signal processor so that said programs stored in said external memory part can be forwarded to said internal memory part.

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11. The modem as claimed in 10, wherein
said clock control part restarts outputting said
clock signal to said digital signal processor after
said forward circuit part supplies said clock control
5 part with a signal requesting that said clock control
part outputs said clock signal to said digital signal
processor to said clock control part, after said
programs stored in said external memory part are
completely forwarded to said internal memory part.

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12. The modem as claimed in 8, wherein said
15 clock signal control part controls outputting of said
clock signal to said digital signal processor in
compliance with a request from said digital signal
processor.

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13. The modem as claimed in 8, wherein said
clock signal control part controls outputting of said
25 clock signal to said digital signal processor in

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14. The modem as claimed in 8, wherein said clock signal control part comprises a forward circuit for forwarding a desired part of said programs read from said external memory part to said internal memory.